FIG.3A

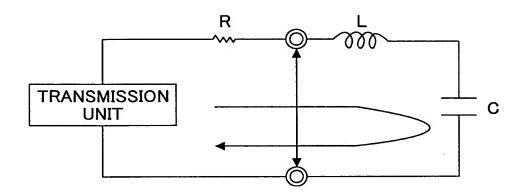
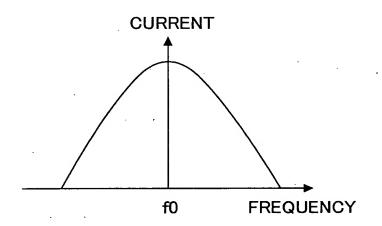


FIG.3B



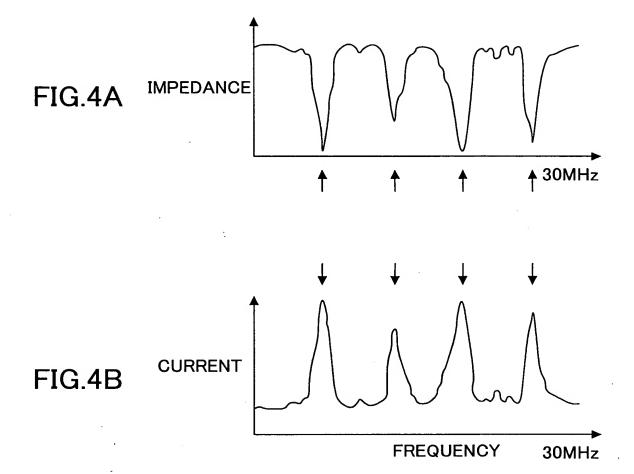


FIG.5

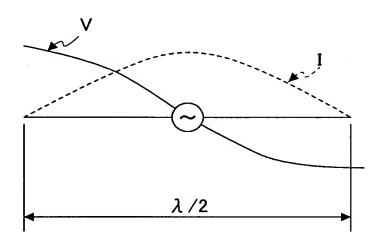
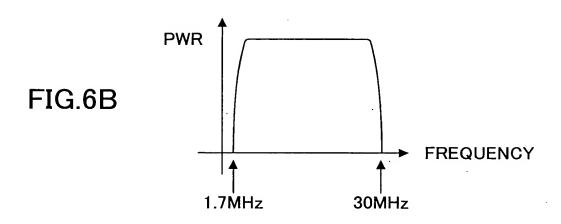
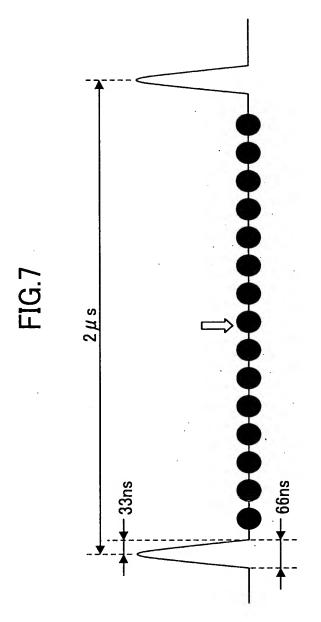


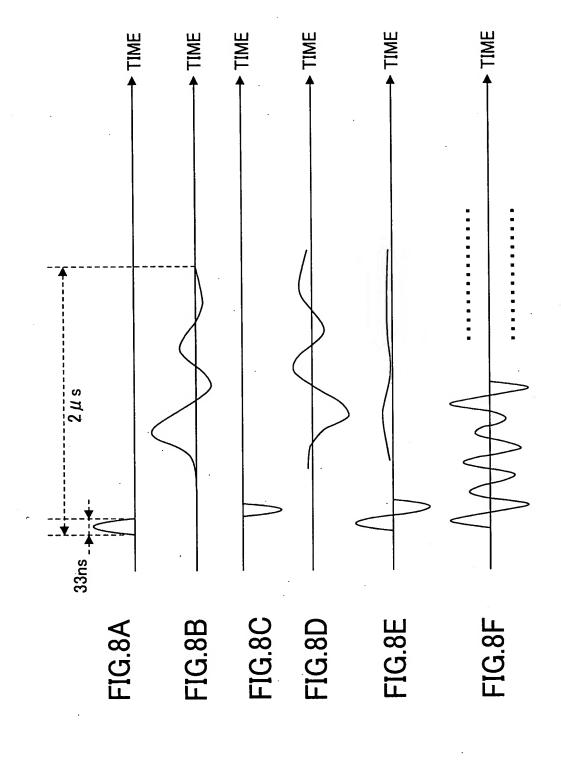
FIG.6A











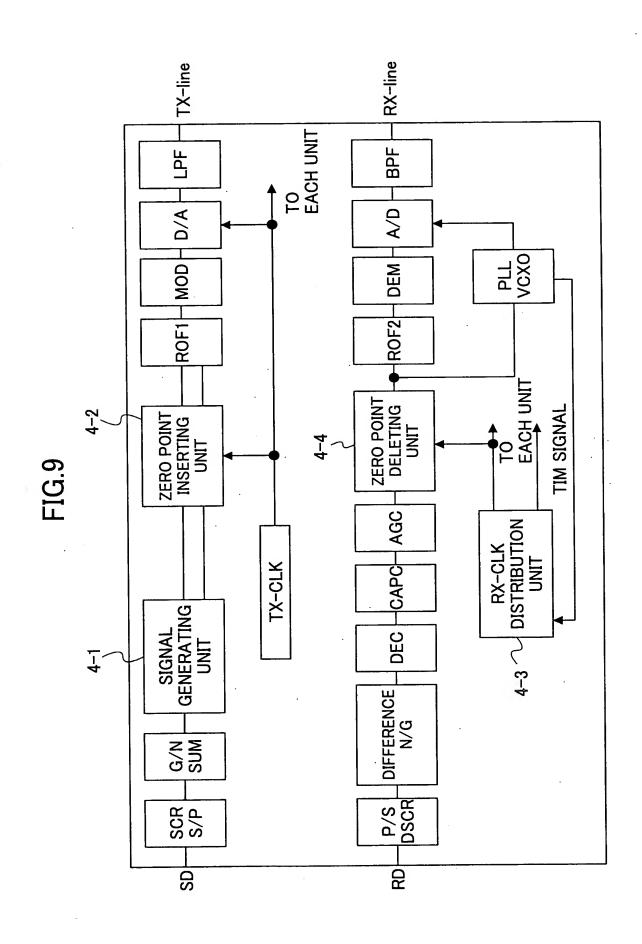
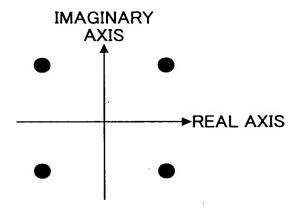
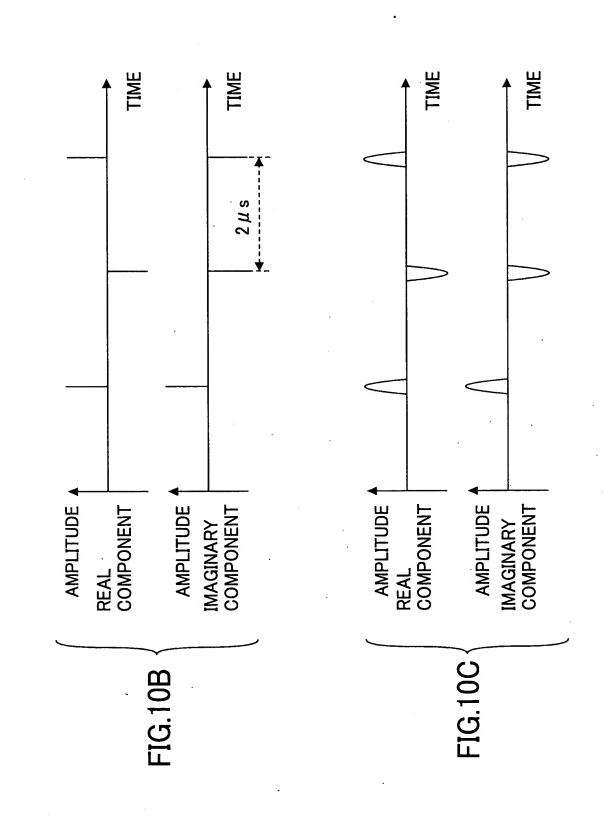
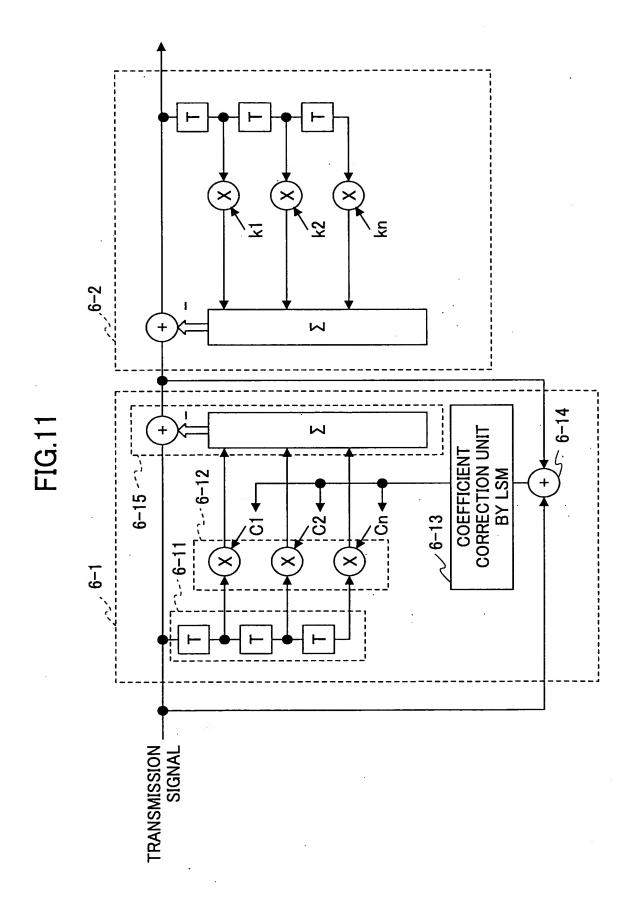


FIG.10A







BPF HRX-line TX-line TO EACH UNIT LPF D/A A/D MULTI-PATH FQUALIZING – UNIT ROF2H DEM 6-1 H MOD -ROF1 ZERO POINT DELETING UNIT ZERO POINT
- INSERTING -4-2 (OFDM) 7-3 FFT GUARD - AGC TX-CLK (OFDM) DEC H CAPC 7-1 — SIGNAL HGENERATING UNIT 4-1 P/S DIFFERENCE DSCR N/G SUM SUM SCR S/P **B** SDH

VCXO

TO EACH UNIT

RX-CLK DISTRIBUTION UNIT

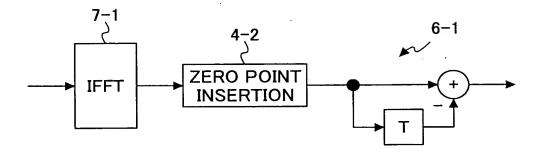
4-3

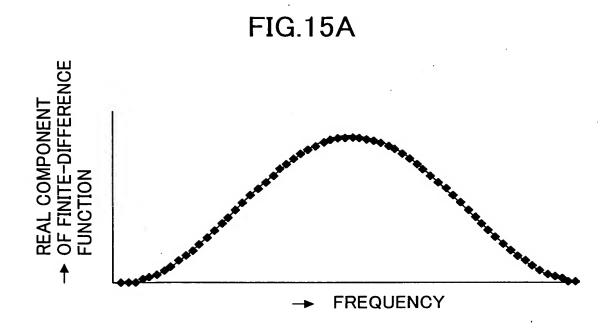
TIM SIGNAL

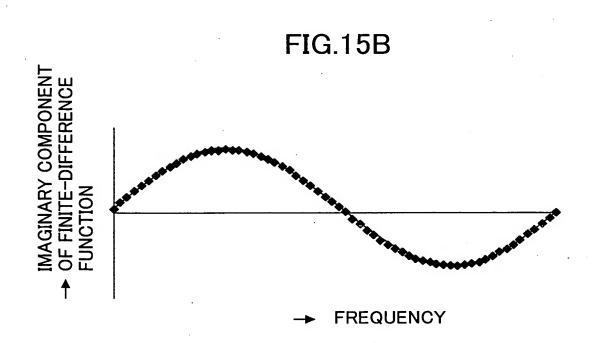
FIG.12

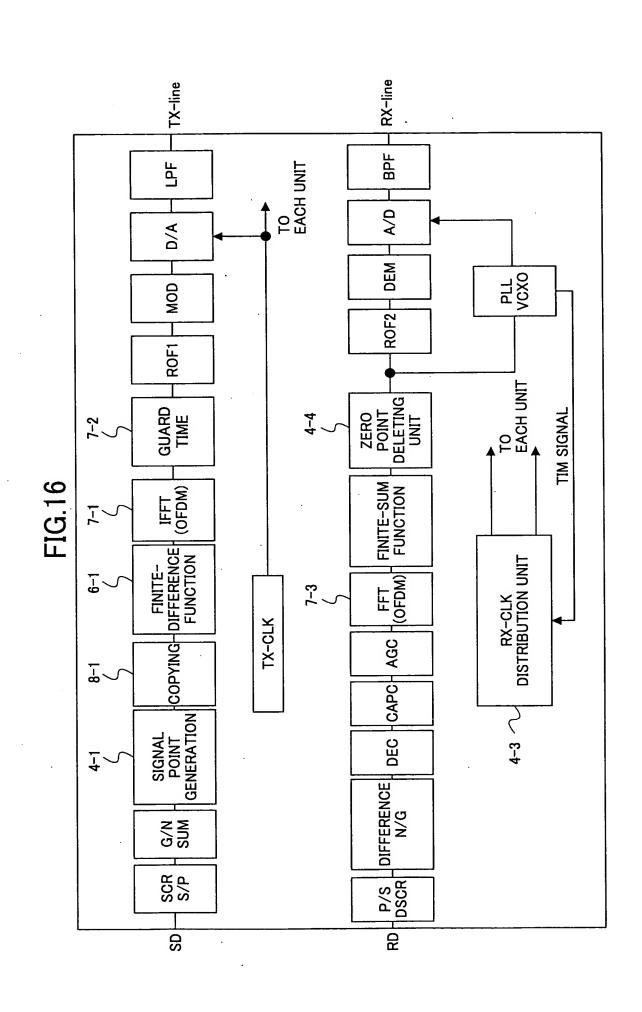
FIG.13

FIG.14









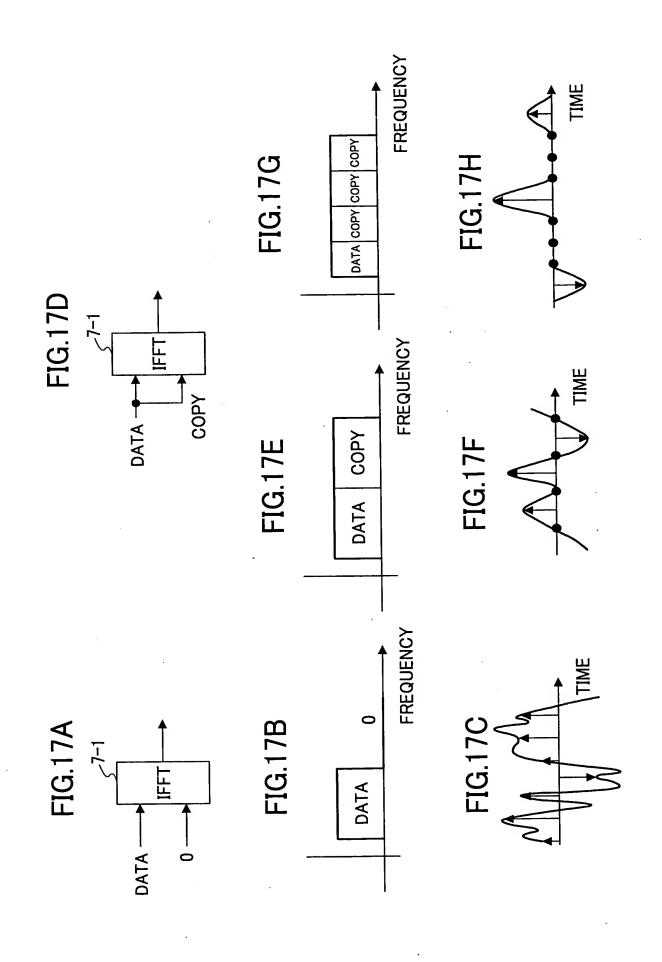


FIG.18

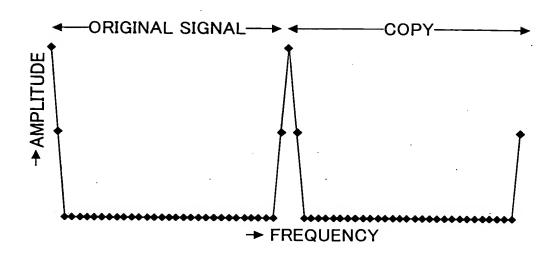
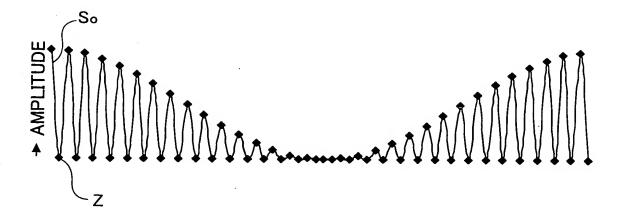


FIG.19



→ TIME AXIS

FIG.20

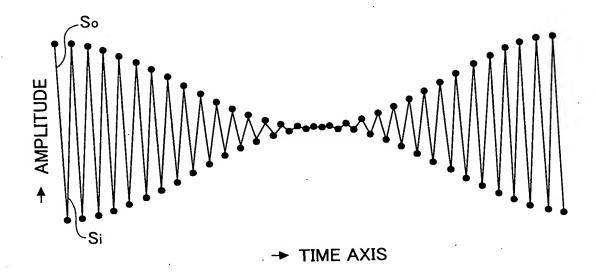


FIG.21

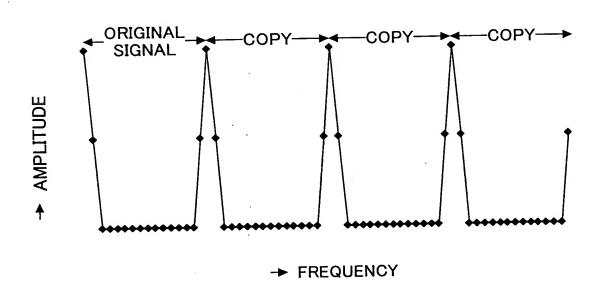


FIG.22

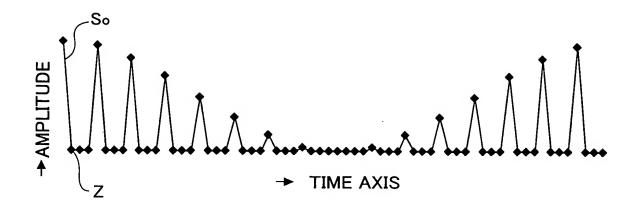


FIG.23

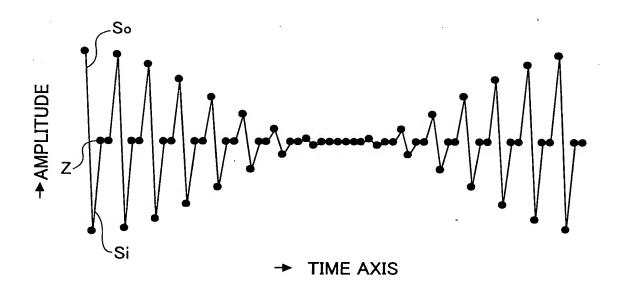


FIG.24

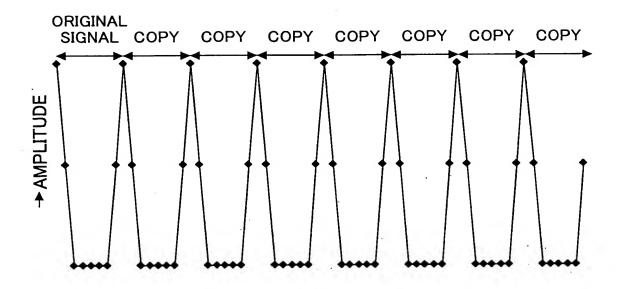


FIG.25

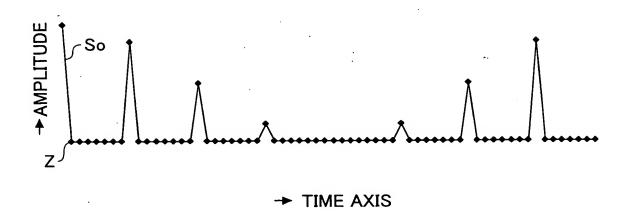


FIG.26

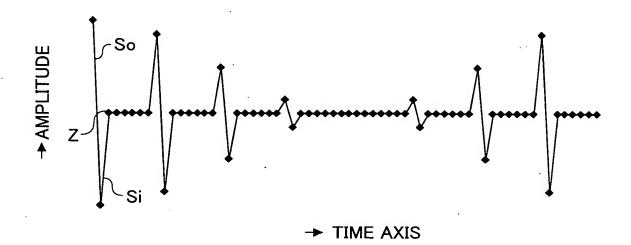


FIG.27A

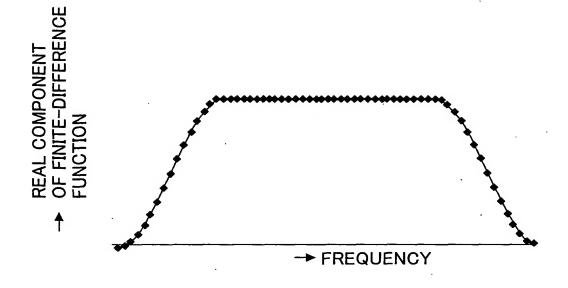


FIG.27B

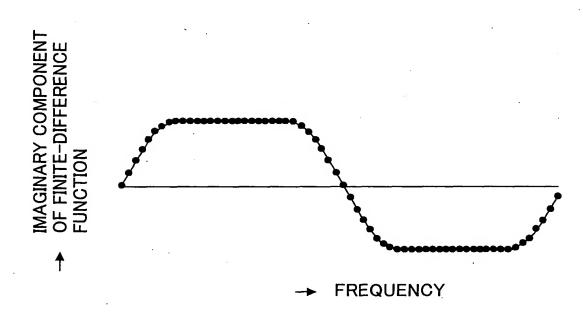


FIG.28A

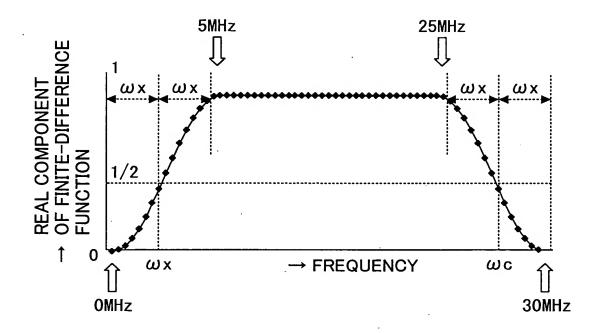


FIG.28B

